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Communications, IEEE Transactions on [legacy, pre - 1988] , Volume: 16 , Issue: 4 , Aug 1968  
Pages:567 - 575

[\[Abstract\]](#)   [\[PDF Full-Text \(776 KB\)\]](#)   **IEEE JNL**

## 2 A block organized 64-kbit CCD memory

Varshney, R.C.; Venkataswaran, K.;

Solid-State Circuits, IEEE Journal of , Volume: 13 , Issue: 5 , Oct 1978  
Pages:681 - 687

[\[Abstract\]](#)   [\[PDF Full-Text \(1304 KB\)\]](#)   [IEEE JNL](#)

### 3 Design of a single-chip digital lattice equalizer filter

*Kanopoulos, N.; Konstantinides, K.;*

Solid-State Circuits, IEEE Journal of , Volume: 20 , Issue: 6 , Dec 1985  
Pages:1235 - 1241

[\[Abstract\]](#)   [\[PDF Full-Text \(712 KB\)\]](#)   **IEEE JNL**

#### 4 Transactions Papers - OFDM or single-carrier block transmissions?

*Zhengdao Wang; Xiaoli Ma; Giannakis, G.B.;*

Communications, IEEE Transactions on , Volume: 52 , Issue: 3 , March 2004  
Pages:380 - 394

[\[Abstract\]](#)   [\[PDF Full-Text \(536 KB\)\]](#)   **IEEE JNL**

**5 On the design of space-time and space-frequency codes for MIMO frequency-selective fading channels**

*El Gamal, H.; Hammons, A.R.Jr.; Youjian Liu; Fitz, M.P.; Takeshita, O.Y.;*

Information Theory, IEEE Transactions on , Volume: 49 , Issue: 9 , Sept. 2000  
Pages:2277 - 2292

[\[Abstract\]](#) [\[PDF Full-Text \(832 KB\)\]](#) IEEE JNL

**6 Low-rate channel coding with complex-valued block codes**

*Dekorsy, A.; Kuehn, V.; Kammeyer, K.-D.;*

Communications, IEEE Transactions on , Volume: 51 , Issue: 5 , May 2003  
Pages:800 - 809

[\[Abstract\]](#) [\[PDF Full-Text \(756 KB\)\]](#) IEEE JNL

**7 VLSI implementation of a 100- $\mu$ W multirate FSK receiver**

*Grayver, E.; Daneshrad, B.;*

Solid-State Circuits, IEEE Journal of , Volume: 36 , Issue: 11 , Nov. 2001  
Pages:1821 - 1828

[\[Abstract\]](#) [\[PDF Full-Text \(448 KB\)\]](#) IEEE JNL

**8 On interleaved, differentially encoded convolutional codes**

*Peleg, M.; Sason, I.; Shamai, S.; Elia, A.;*

Information Theory, IEEE Transactions on , Volume: 45 , Issue: 7 , Nov. 1999  
Pages:2572 - 2582

[\[Abstract\]](#) [\[PDF Full-Text \(328 KB\)\]](#) IEEE JNL

**9 Design of digital filters for advanced telecommunications ASIC's using special-purpose silicon compiler**

*Balestro, F.; Chianale, A.; Privat, G.; Tawfik, M.S.; Vandeweerd, I.; Wittmann, A.D.;*

Solid-State Circuits, IEEE Journal of , Volume: 26 , Issue: 7 , July 1991  
Pages:1047 - 1055

[\[Abstract\]](#) [\[PDF Full-Text \(940 KB\)\]](#) IEEE JNL

**10 A 1-GIPS Josephson data processor**

*Hatano, Y.; Yano, S.; Mori, H.; Yamada, H.; Hirano, M.;*

Solid-State Circuits, IEEE Journal of , Volume: 26 , Issue: 6 , June 1991  
Pages:880 - 883

[\[Abstract\]](#) [\[PDF Full-Text \(352 KB\)\]](#) IEEE JNL

**11 A hybrid digital and analog controller for DC and brushless servom**

*Yokote, H.; Watanabe, K.;*

Instrumentation and Measurement, IEEE Transactions on , Volume: 39 , Issue: 1 , Feb 1990  
Pages:259 - 263

[\[Abstract\]](#) [\[PDF Full-Text \(388 KB\)\]](#) IEEE JNL

**12 RSFQ logic arithmetic**

*Mukhanov, O.A.; Rylov, S.V.; Semonov, V.K.; Vyshenskii, S.V.;*  
 Magnetics, IEEE Transactions on , Volume: 25 , Issue: 2 , Mar 1989  
 Pages:857 - 860

[\[Abstract\]](#)   [\[PDF Full-Text \(300 KB\)\]](#)   IEEE JNL

**13 A DC measurement IC with 130 nV<sub>pp</sub> noise in 10 Hz**

*Thomsen, A.; de Angel, E.; Wu, S.X.; Amar, A.; Lei Wang; Wai Lee;*  
 Solid-State Circuits Conference, 2000. Digest of Technical Papers. ISSCC. 2000  
 IEEE International , 7-9 Feb. 2000  
 Pages:334 - 335

[\[Abstract\]](#)   [\[PDF Full-Text \(238 KB\)\]](#)   IEEE CNF

**14 Space-time bit-interleaved coded modulation over frequency selective fading channels with iterative decoding**

*Tonello, A.M.;*  
 Global Telecommunications Conference, 2000. GLOBECOM '00. IEEE , Volume 3 , 27 Nov.-1 Dec. 2000  
 Pages:1616 - 1620 vol.3

[\[Abstract\]](#)   [\[PDF Full-Text \(512 KB\)\]](#)   IEEE CNF

**15 A 4.25 GHz BiCMOS clock recovery circuit with an AV-DSPD architecture for NRZ data stream**

*Nakamura, S.; Tajima, A.; Kinoshita, Y.; Suemura, Y.; Fukaishi, M.; Suzuki, I.; Itani, T.; Miyamoto, H.; Henmi, N.; Yamazaki, T.; Yotsuyanagi, M.;*  
 Solid-State Circuits Conference, 1997. Digest of Technical Papers. 44th ISSCC 1997 IEEE International , 6-8 Feb. 1997  
 Pages:248 - 249, 467

[\[Abstract\]](#)   [\[PDF Full-Text \(1060 KB\)\]](#)   IEEE CNF

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